

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
2 February 2006 (02.02.2006)

PCT

(10) International Publication Number  
**WO 2006/011680 A1**

(51) International Patent Classification<sup>7</sup>: **H03L 7/22**,  
H04L 7/00

(21) International Application Number:  
PCT/JP2005/014360

(22) International Filing Date: 29 July 2005 (29.07.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
2004-223887 30 July 2004 (30.07.2004) JP

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

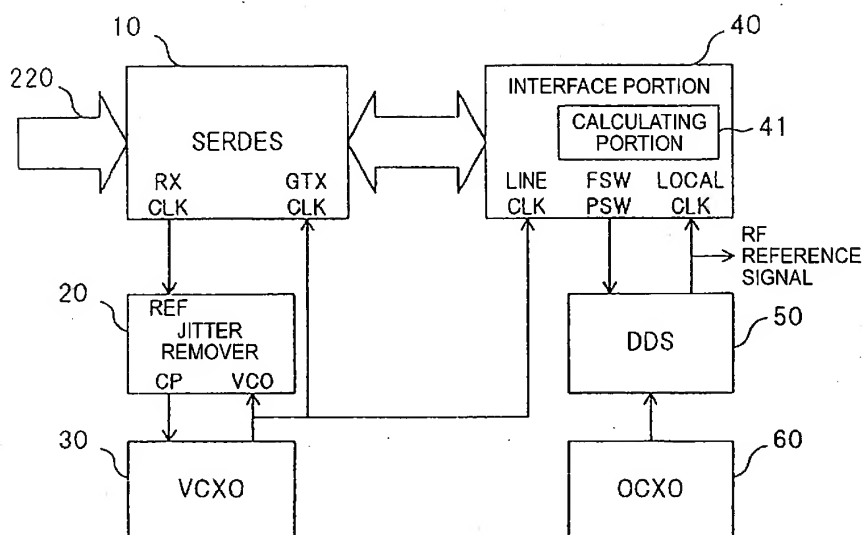
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: **SYNCHRONOUS FOLLOW-UP APPARATUS AND SYNCHRONOUS FOLLOW-UP METHOD**



(57) Abstract: A synchronous follow-up apparatus includes a PLL portion that outputs a first clock signal, and a control loop portion. The control loop portion includes a reference frequency signal generating portion that outputs a reference frequency signal, a clock signal generating portion that generates a second clock signal having the same frequency as a frequency of the first clock signal based on the reference frequency signal generated by the reference frequency signal generating portion, and a frequency control signal generating portion that generates a frequency control signal to change the frequency of the second clock signal based on a frequency difference between the first clock signal and the second clock signal that occurs after a predetermined time and outputs the frequency control signal to the clock signal generating portion.